

International Symposium & Workshop on Many-Core Computing: Hardware and Software 17-18th January 2018, Chilworth Manor, Southampton, UK

Wednesday 17th January		Speaker	Title	Affiliation
10.30 onwards		Arrival & Registration		
11.15 - 11.30	Welcome and Introduction		Bashir Al-Hashimi	University of Southampton
11.30 - 12.00	Talk 1	<i>Nikil Dutt</i>	Adaptive, Reflective Middleware for Power and Energy Management in Many-Core Heterogeneous Systems	University of California, Irvine
12.00 - 12.30	Talk 2	<i>Onur Mutlu</i>	Rethinking Memory System Design for Data-Intensive Computing	ETH Zurich
12.30 - 1.30		LUNCH		
1.30 - 2.00	Talk 3	<i>Olaf Spinczyk</i>	The MxKernel	TU Dortmund University
2.00 - 2.30	Talk 4	<i>Sam Ainsworth</i>	Parallel Support Cores for Performance and Reliability	University of Cambridge
2.30 - 3.00	Talk 5	<i>Michael O'Boyle</i>	Heterogeneous Thinking	University of Edinburgh
3-3.30		COFFEE		
3.30 - 4.00	Talk 6	<i>John McAllister</i>	FPGA Smart Dust	Queen's University Belfast
4.00 - 4.30	Talk 7	<i>Dan Fay</i>	The Catapult Project – An FPGA view of the Data Center	Microsoft
4.30 - 5.00	Talk 8	<i>Stephan Diestelhorst</i>	Benefits, Trade-offs, and Challenges of Pushing Heterogeneous Cores Together	ARM
5.00 - 5.30	Talk 9	<i>Guihai Yan</i>	Reshaping Manycore for Deep Learning	Institute of Computing Technology, Chinese Academy of Sciences
5.30 - 6.00	Talk 10	<i>David Gregg</i>	Parallel Multi Channel Convolution using General Matrix Multiplication	Trinity College Dublin
6.30 - 7.30		Reception with opportunity to view PRiME Demonstrators – Earl Jellicoe Room		
7.30 onwards		Symposium Dinner - Lord Selbourne Room		
Thursday 18th January		Speaker	Title	Affiliation
9.00 - 9.30	Talk 11	<i>Luca Benini</i>	Scaling up Edge Computing with PULP; A Many-Core Platform for Micropower In-Sensor Analytics	ETH Zurich
9.30 - 10.00	Talk 12	<i>Yu Hao</i>	Smart I/Os for Manycore	Southern University of Science and Technology
10.00 - 10.30	Talk 13	<i>Steve Furber</i>	SpiNNaker: Biologically-Inspired Massively-Parallel Computing	University of Manchester
10.30 - 11		COFFEE		
11.00 - 11.30	Talk 14	Andrew Brown	POETS: Partially Ordered Event Triggered Systems	University of Southampton
11.30 - 1.00	Panel - Chair Alex Yakovlev - Design for Trillions of Smart Objects: What are Key Research Drivers?			University of Newcastle
	Panellist	<i>Luca Benini</i>		ETH Zurich
	Panellist	<i>Edith Beigne</i>		CEA-LETI
	Panellist	<i>Andrew Brown</i>		University of Southampton
	Panellist	<i>Kerstin Eder</i>		University of Bristol
1.00 - 2.00	LUNCH			
	2.00 - 2.30	Talk 15	<i>George Constantinides</i>	High-Level Synthesis: Crossing the Hardware / Software Boundary
2.30 - 3.00	Talk 16	<i>Bashir Al-Hashimi</i>	PRiME: Power-efficient, Reliable, Many-core Embedded systems	University of Southampton
3.00 - 4.30	Panel - Chair Björn Franke - Programming Languages/Compilers and OS for Many-Core Computing			University of Edinburgh
	Panellist	<i>Michael Butler</i>		University of Southampton
	Panellist	<i>Dimitrios S. Nikolopoulos</i>		Queen's University Belfast
	Panellist	<i>Paul Kelly</i>		Imperial College London
	Panellist	<i>Ashish Darbari</i>		OneSpin
4.30	Panel - Chair Björn Franke - Programming Languages/Compilers and OS for Many-Core Computing			University of Glasgow
	Workshop Closes			

